



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2812
\$

Applicant: Craig T. Salling, et al.

Art Unit: 2812

Serial No.: 10/051,962

Examiner: Ron E. Pompey

Filed: 01/16/02

Docket: TI-32535

For: Eliminating Substrate Noise by an Electrically Isolated High-Voltage I/O Transistor

Commissioner for Patents
P.O Box 1450
Alexandria, VA 22313-1450

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service on 10-14-03 as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Karen Vertz 10-14-03
Karen Vertz Date

EXTENSION OF TIME

Pursuant to 37 CFR 1.136(a), Applicants respectfully petition the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.
The fee for this extension is indicated below:

- ☒ First Month (\$110)
- ☐ Second Month (\$420)
- ☐ Third Month (\$950)
- ☐ Fourth Month (\$1,480)
- ☐ Fifth Month (\$2,010)

Please charge the fee to deposit account No. 20-0668 of Texas Instruments Incorporated. The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Account No. 20-0668. An original and two copies of this sheet are enclosed

Texas Instruments, Incorporated
P. O. Box 655474 - M/S 3999
Patent Department
Dallas, Texas 75265

Respectfully submitted
Gary C. Honeycutt
Gary C. Honeycutt
Registration No. 20,250

RECEIVED
OCT 23 2003
TECHNOLOGY CENTER 2800